REMARKS

Claims 1-100 are pending. In this Response, claims 61-90 have been withdrawn.

I. RESTRICTION REQUIREMENT

The Examiner asserts that the application contains claims directed to the following patentably distinct species: "Any one of the species or one set of the same species in figures 1A to 22."

The Examiner requires that Applicant elect a single disclosed species for prosecution on the merits and include an identification of the species that is elected consonant with this requirement and a listing of all claims readable thereon.

Applicant provisionally elects claims 1-60 and 91-100 (Figure 19) for prosecution on the merits. Applicant also traverses the restriction requirement and requests that it be withdrawn.

Claims 1-100 are directed to a three-dimensional stacked semiconductor package device includes (1) a first semiconductor package device including (a) a first insulative housing with a first top surface, a first bottom surface, and a first peripheral side surface between the first top and bottom surfaces, (b) a first semiconductor chip within the first insulative housing, wherein the first chip includes a first upper surface and a first lower surface, and the first upper surface includes a first conductive pad, and (c) a first lead that protrudes laterally from and extends through the first peripheral side surface and is electrically connected to the first pad, wherein the first lead outside the first insulative housing is bent downwardly, (2) a second semiconductor package device including (a) a second insulative housing with a second top surface, a second bottom surface, and a second peripheral side surface between the second top and bottom surfaces, (b) a second semiconductor chip within the second insulative housing, wherein the second chip includes a second conductive pad, and (c) a second lead that protrudes laterally from and extends through the second peripheral side surface and is electrically connected to the second pad, wherein the second lead outside the second insulative housing is flat, and (3) a conductive

bond outside the insulative housings that contacts and electrically connects the leads, wherein the second insulative housing overlaps the first insulative housing, the second lead overlaps the first lead outside the insulative housings, the top surfaces face upwardly, the bottom surfaces face downwardly, and the first top surface faces towards the second bottom surface.

Thus, claims 1-100 are highly related to one another.

The claims correspond to the figures as follows:

| Species | <u>Figure</u> | <u>Claims</u> |
|----------------|---------------|-----------------|
| I | 19 | 1-60 and 91-100 |
| II | 20 | 61-70 |
| III | 21 | 71-80 |
| IV | 22 | 81-90 |

Claims 1, 11, 21, 31, 41, 51 and 91 (Figure 19) are directed to a stacked device that includes first (lower) and second (upper) semiconductor package devices. Claim 61 (Figure 20) is directed to a stacked device that includes a third semiconductor package device above the second semiconductor package device. Claim 71 (Figure 21) is directed to a stacked device that includes a third semiconductor package device that extends into a cavity in the first semiconductor package device. Claim 81 (Figure 22) is directed to a stacked device that includes a third semiconductor package device that extends into a cavity in the second semiconductor package device.

There <u>must</u> be a serious burden on the Examiner if the restriction is required (M.P.E.P. § 803). Where the related inventions as claimed are shown to be distinct, the Examiner, in order to establish reasons for insisting upon restriction, <u>must</u> show by appropriate explanation one of the following: (A) separate classification thereof; (B) a separate status in the art when they are classifiable together; or (C) a different field of search (M.P.E.P. § 808.02).

The Examiner has not even attempted to explain how examining the species I-IV would involve separate classification, separate status in the art, or a different field of search (M.P.E.P. § 808.02). Therefore, the Examiner has ignored mandatory elements set forth in the M.P.E.P. and the restriction requirement is improper.

The Examiner has the burden of establishing that the restriction requirement is justified. The mandatory elements set forth in the M.P.E.P. prevent the Examiner from having unfettered discretion over issuing restriction requirements that force Applicant to file divisional applications and absorb the related costs and delays in order to have the non-elected claims considered on the merits. If, however, the Examiner can obtain a restriction requirement merely by asserting that different embodiments are different species then these mandatory elements are easily circumvented and rendered meaningless.

II. CONCLUSION

In view of the remarks set forth herein, the application is believed to be in condition for allowance. Should any issues remain, the Examiner is encouraged to telephone the undersigned attorney.

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on June 29, 2004

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Respectfully submitted,